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Docket No.: 57454-072

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JC979 U.S. PTO
09/829046
04/10/01



In re Application of :
Shigeki OHBAYASHI :
Serial No.: Group Art Unit:
Filed: April 10, 2001 : Examiner:
For: STATIC SEMICONDUCTOR MEMORY DEVICE HAVING T-TYPE BIT LINE
STRUCTURE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

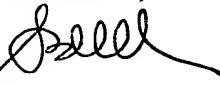
Please note that U.S. Patent 5,808,930 is a counterpart of Japanese Patent 9-162305 and U.S. Patent 6,088,276 is a counterpart of Japanese Patent 11-306762.

Serial No.:

Each non-English language reference is accompanied by an English abstract.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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